

IN THE SPECIFICATION:

Please amend Paragraph [0004] on Page 2 to read as follows:

[0004] According to one aspect of the invention, there is provided a method for limiting a signal in a transmitter at chip level, the method comprising: ~~It is an object of the invention to provide an improved method and apparatus for limiting power or amplitude values of a signal. This is achieved with a method for limiting a signal in a transmitter at chip level. The method of the invention comprises~~ determining a limiting signal from a transmissible signal filtered using a pulse shaping filter, determining an error signal using the transmissible signal and the limiting signal, generating a limited transmissible signal by reducing an error signal filtered using the filter matched to a chip pulse waveform from the transmissible signal.

Please amend Paragraph [0005] on Page 2 to read as follows:

[0005] ~~The invention also relates to a method for limiting a signal in a transmitter at chip level. The method according to the invention comprises~~ According to another aspect of the invention, there is provided a method for limiting a signal in a transmitter at chip level, the method comprising: determining a limiting signal from a transmissible signal filtered using a pulse shaping filter, determining an error signal using the transmissible signal and the limiting signal, orthogonalizing the error signal filtered using the filter matched to a chip pulse waveform, generating a limited transmissible signal by reducing the orthogonalized error signal from the transmissible signal.

Please amend Paragraph [0006] on Page 2 to read as follows:

[0006] ~~The invention further relates to a method for limiting a signal in a transmitter at chip level. The method according to the invention comprises~~ According to another aspect of the invention, there is provided a method for limiting a signal in a transmitter at chip level, the method comprising: combining at least two signals modulated on different carriers to a combination signal, determining a limiting signal from the combination signal filtered using a pulse shaping filter, determining an error signal using the combination signal and the limiting signal, dividing the error signal onto different carriers in a predetermined manner, generating limited transmissible signals by reducing each error signal part filtered using the filter matched to a chip pulse waveform from a corresponding transmissible signal.

Please amend Paragraph [0007] on Page 2 to read as follows:

[0007] ~~The invention also relates to a transmitter limiting a signal at chip level. The transmitter comprises~~ According to another aspect of the invention, there is provided a transmitter limiting a signal at chip level, the transmitter comprising: means for determining a limiting signal from a transmissible signal filtered using a pulse shaping filter, ~~the transmitter comprises~~ means for determining an error signal using the transmissible signal and the limiting signal, ~~the transmitter comprises~~ means for generating a limited transmissible signal by reducing the error signal filtered using the filter matched to a chip pulse waveform from the transmissible signal, ~~the transmitter~~

~~comprises~~ means for filtering the limited transmissible signal using the pulse shaping filter.

Please amend Paragraph [0008] beginning on line 35 of Page 2 and ending on line 15 of Page 3 to read as follows:

[0008] ~~The invention further relates to a transmitter limiting a signal at chip level. The transmitter comprises~~ According to another aspect of the invention, there is provided a transmitter limiting a signal at chip level, the transmitter comprising: means for determining a first limiting signal from a transmissible signal filtered using a pulse shaping filter, ~~the transmitter comprises~~ means for determining a first error signal using the transmissible signal and the first limiting signal, ~~the transmitter comprises~~ means for orthogonalizing the first error signal filtered using the filter matched to a chip pulse waveform, ~~the transmitter comprises~~ means for generating a first limited transmissible signal by reducing the orthogonalized first error signal from the transmissible signal, ~~the transmitter comprises~~ means for determining a second limiting signal from the first limited transmissible signal filtered using the pulse shaping filter, ~~the transmitter comprises~~ means for determining a second error signal using the first limited transmissible signal and the second limiting signal, ~~the transmitter comprises~~ means for generating a second limited transmissible signal by reducing the second error signal filtered using the filter matched to a chip pulse waveform from the transmissible signal, ~~the transmitter comprises~~ means for filtering the second limited transmissible signal using the pulse shaping filter.

Please amend Paragraph [0009] on Page 3 to read as follows:

[0009] ~~The invention also relates to a transmitter limiting a signal at chip level.~~
~~The transmitter comprises~~ According to another aspect of the invention, there is provided
a transmitter limiting a signal at chip level, the transmitter comprising: means for
combining at least two signals modulated on different carriers to a combination signal,
~~the transmitter comprises~~ means for determining a limiting signal from the combination
signal filtered using a pulse shaping filter, ~~the transmitter comprises~~ means for
determining an error signal using the combination signal and the limiting signal, ~~the~~
~~transmitter comprises~~ means for dividing the error signal onto different carriers in a
predetermined manner, ~~the transmitter comprises~~ means for generating limited
transmissible signals by reducing each error signal part filtered using the filter matched to
a chip pulse waveform from a corresponding transmissible signal, ~~the transmitter~~
~~comprises~~ means for filtering the limited transmissible signals using the pulse shaping
filter, ~~the transmitter comprises~~ means for generating a combined limited transmissible
signal by combining the filtered limited transmissible signals.

Please add the following paragraphs between Paragraphs [0009] and [00010] on
Page 3 to read as follows:

[0009.1] According to another aspect of the invention, there is provided a
transmitter limiting a signal at chip level, the transmitter being configured to: determine a
limiting signal from a transmissible signal filtered using a pulse shaping filter, determine
an error signal using the transmissible signal and the limiting signal, generate a limited

transmissible signal by reducing the error signal filtered using the filter matched to a chip pulse waveform from the transmissible signal, filter the limited transmissible signal using the pulse shaping filter.

[0009.2] According to another aspect of the invention, there is provided a transmitter limiting a signal at chip level, the transmitter being configured to: determine a first limiting signal from a transmissible signal filtered using a pulse shaping filter, determine a first error signal using the transmissible signal and the first limiting signal, orthogonalize the first error signal filtered using the filter matched to a chip pulse waveform, generate a first limited transmissible signal by reducing the orthogonalized first error signal from the transmissible signal, determine a second limiting signal from the first limited transmissible signal filtered using the pulse shaping filter, determine a second error signal using the first limited transmissible signal and the second limiting signal, generate a second limited transmissible signal by reducing the second error signal filtered using the filter matched to a chip pulse waveform from the transmissible signal, filter the second limited transmissible signal using the pulse shaping filter.

[0009.3] According to another aspect of the invention, there is provided a transmitter limiting a signal at chip level, the transmitter being configured to: combine at least two signals modulated on different carriers to a combination signal, determine a limiting signal from the combination signal filtered using a pulse shaping filter, determine an error signal using the combination signal and the limiting signal, divide the error signal onto different carriers in a predetermined manner, generate limited transmissible signals by reducing each error signal part filtered using the filter matched to

a chip pulse waveform from a corresponding transmissible signal, filter the limited transmissible signals using the pulse shaping filter, generate a combined limited transmissible signal by combining the filtered limited transmissible signals.

[0009.4] According to another aspect of the invention, there is provided a transmitter limiting a signal at chip level, the transmitter being configured to: filter transmissible signals modulated on different carriers using pulse shaping filters, combine at least two filtered signals to a combination signal. determine a limiting signal from the combination signal, determine an error signal using the combination signal and the limiting signal, divide the error signal onto different carriers in a predetermined manner, generate limited transmissible signals by reducing each error signal part filtered using the filter matched to a chip pulse waveform from a corresponding transmissible signal, filter the limited transmissible signals using the pulse shaping filter, generate a combined limited transmissible signal by combining the filtered limited transmissible signals.